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Nov 4 2009
October 9, 2009

Assistant Commissioner
of Patents
Washington, D.C. 20231

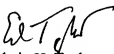
Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,448,030
Issued: November 4, 2008
For: Optimized Ordering of Firmware Modules in Pre-
Boot Environment
Inventor: Liu et al.
Our File No. 42390.P17834

Dear Sir:

Enclosed is a Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Edwin H. Taylor
Reg. No. 25,129

EHT/rap
enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,448,030

DATED : November 4, 2008

INVENTOR(S) : Liu et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, at line 54 delete , -- (PEN — and insert "(PEIM)".

In column 10, at line 56 delete , --PEJ, — and insert "PEI".

MAILING ADDRESS OF SENDER
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PATENT NO. 7,448,030

Certificate of Correction (PTO Form 1050)-Amended